



FEATURES

- Slew rate: 0.93V/ μ s
- Bandwidth: 1.6MHz
- Input/Output full swing
- Low supply current: 151 μ A
- Offset Voltage: 0.8mV (Typical)
- Supply Voltage: 2.1V to 6.0V
- Operation Temperature Range: -40°C to 125°C
- Micro Size Packages: SOIC

APPLICATIONS

- Transducers
- Temperature Measurement
- Electronic Scales
- Medical instrumentation
- Handheld Test Equipment
- Battery equipment

- Consumer electronics

GENERAL DESCRIPTION

The MT060X series are single, dual, and quad rail-to-rail CMOS operational amplifiers. These amplifiers have the characteristics of input/output full swing, low offset, low power and stable high frequency response. They achieve very good AC performance with 1.6MHz bandwidth, 0.93V/ μ s slew rate and low distortion while drawing only 83 μ A of quiescent current per amplifier. MT060X has wide temperature range from -40°C to +125°C.

Single or dual supplies as low as 2.1V(\pm 1.05V) and up to 6.0V(\pm 3.0V) can be used.

The MT0602 is available in the 8-Pin SOIC packages.

SIMPLIFIED SCHEMATIC

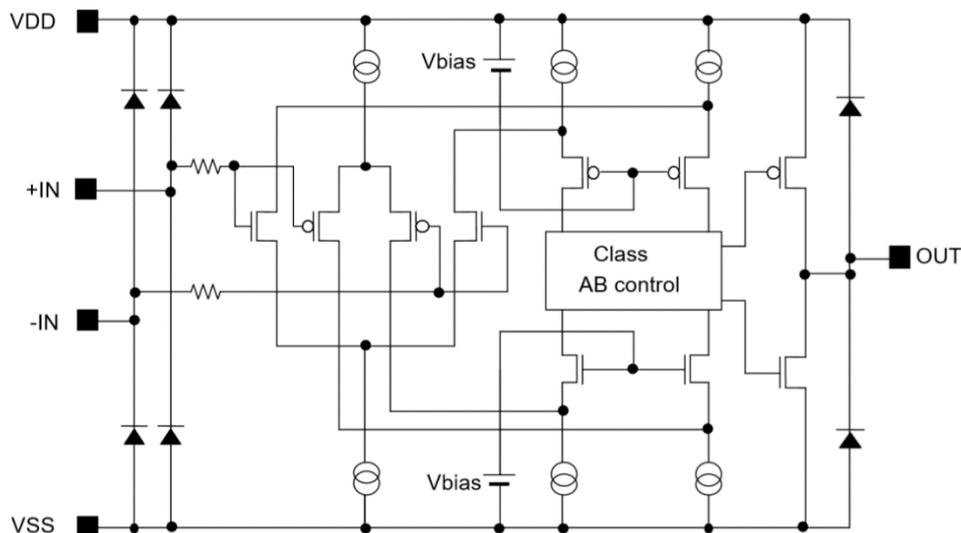


Figure 1. Simplified schematic

ABSOLUTE MAXIMUM RATINGS (Note 1)

Supply Voltage..... +2.1V to +6.0V
 Input Offset Voltage.....0.8mV(typical)
 Input Offset Current.....1.9pA(typical)
 Maximum Operating Junction Temperature.....150°C
 Operating Temperature Range.....-40°C to 125°C
 Storage Temperature-65°C to 150°C

PACKAGE/ORDER INFORMATION

TOP VIEW	Order Part Number	Package	Top Marking
	MT0602	8-Pin SOIC	MT0602AJ

DEVICE INFORMATION

Order Part Number	Top Marking	Package
MT0602	MT0602AJ	SOIC-8

PIN DESCRIPTION

Pin Name	Pin Number	Description
OUTA	-	Output of channel A
-INA	-	Inverting input of channel A
+INA	-	Noninverting input of channel A
-VS	-	Positive (highest) power supply
+INB	-	Noninverting input of channel B
-INB	-	Inverting input of channel B
OUTB	-	Output of channel B
+VS	-	Negative(lowest) power supply

ELECTRICAL CHARACTERISTICS (Note 3)

(At $T_A = 25^\circ\text{C}$, $+V_S = +2.5\text{V}$, $-V_S = -2.5\text{V}$, $R_L = 2\text{K}\Omega$, $C_L = 100\text{pF}$, unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
Input Offset Voltage	$V_S = 5.0\text{V}$	-3	0.8	3	mV
Input Offset Voltage Drift	$T_A = -40^\circ\text{C}$ to 125°C		2		$\mu\text{V}/^\circ\text{C}$
Input Bias Current	$T_A = 25^\circ\text{C}$		1.7		pA
Input Offset Current			1.9		pA
Power Supply Rejection Ratio		80	91		dB
Common-mode Rejection Ratio		70	95		dB
Open Loop Voltage Gain	$R_L = 2\text{K}\Omega$, $C_L = 100\text{pF}$	85	119		dB
Gain-bandwidth product	$R_L = 2\text{K}\Omega$, $C_L = 100\text{pF}$		1.6		MHz
Slew Rate	$G = +1$, $R_L = 1\text{K}\Omega$, $C_L = 56\text{pF}$		0.93		$\text{V}/\mu\text{s}$
Input Voltage Noise	$f = 0.1\text{Hz}$ to 10Hz		8		μV_{PP}
Input Voltage Noise Density	$f = 1\text{kHz}$		27		$\text{nV}/\sqrt{\text{Hz}}$
Supply Current (per amplifier)			151		μA
Operating Temperature Range		-40		125	$^\circ\text{C}$
Storage Temperature Range		-65		150	$^\circ\text{C}$

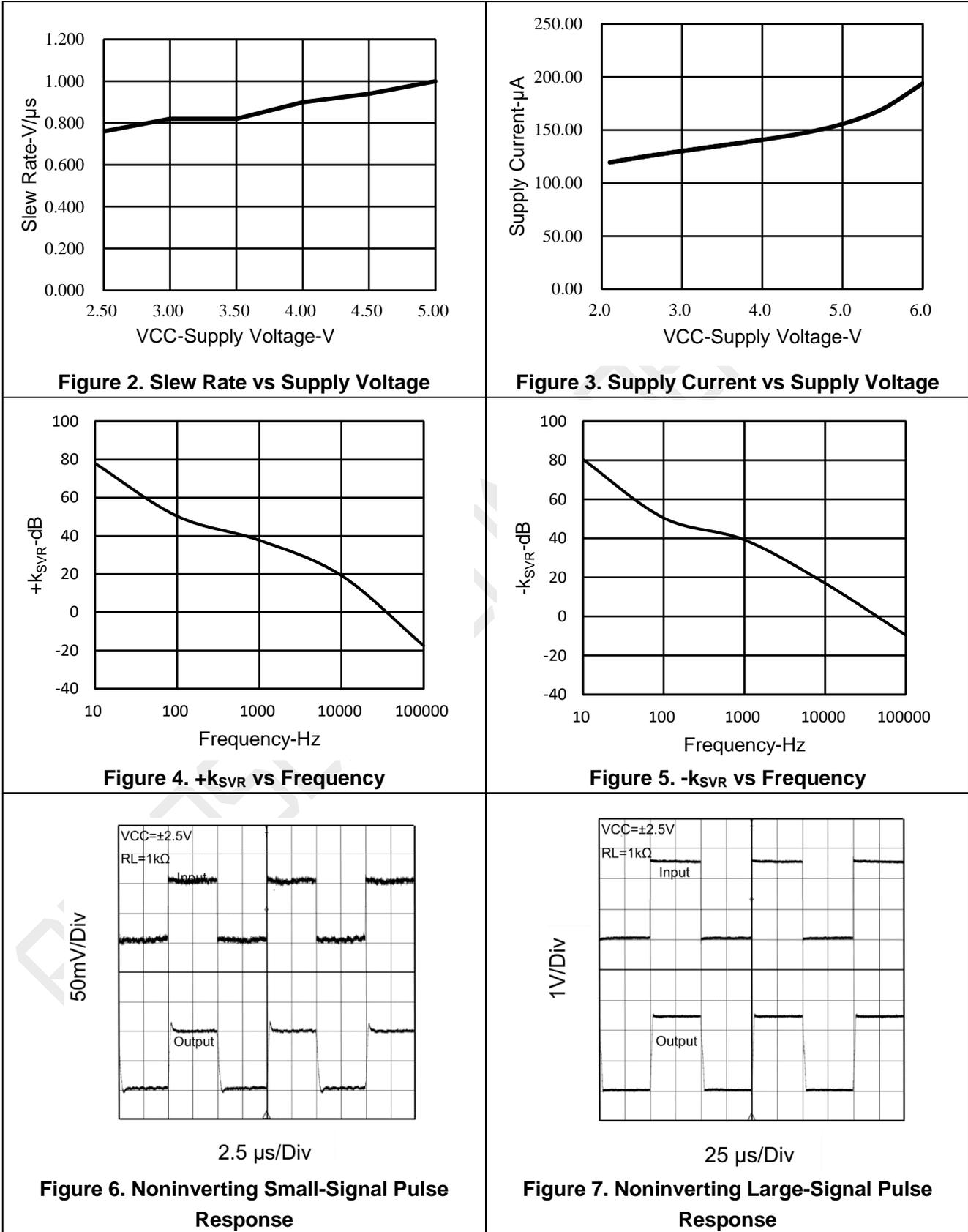
Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: T_J is calculated from the ambient temperature T_A and power dissipation P_D according to the following formula: $T_J = T_A + (P_D) \times (170^\circ\text{C}/\text{W})$.

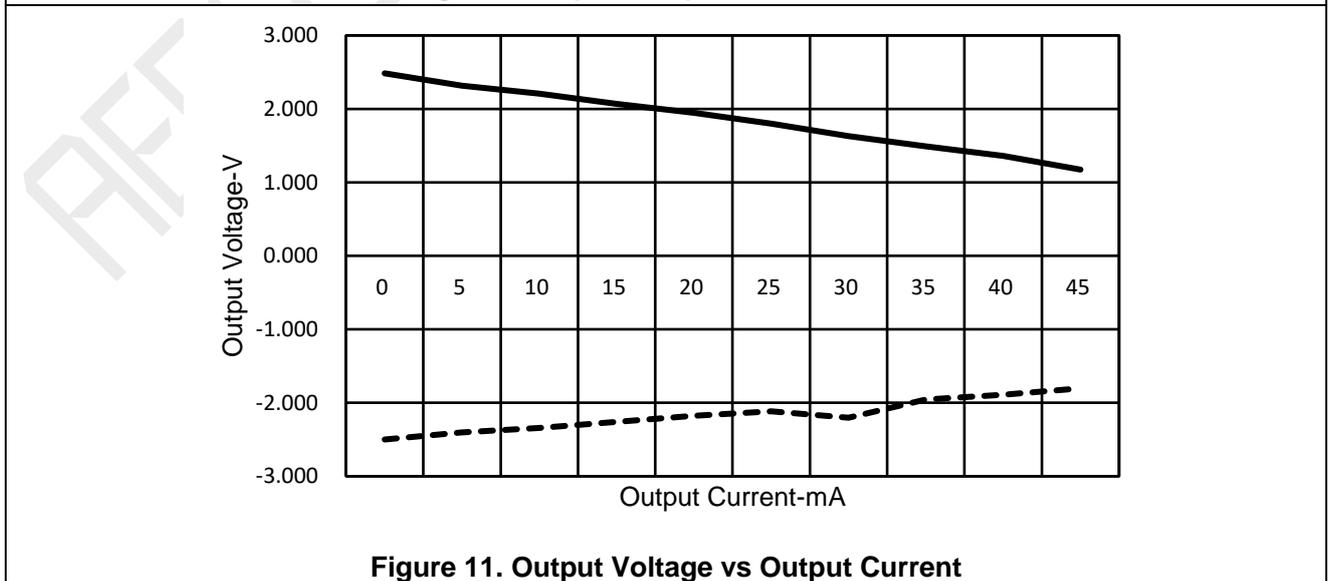
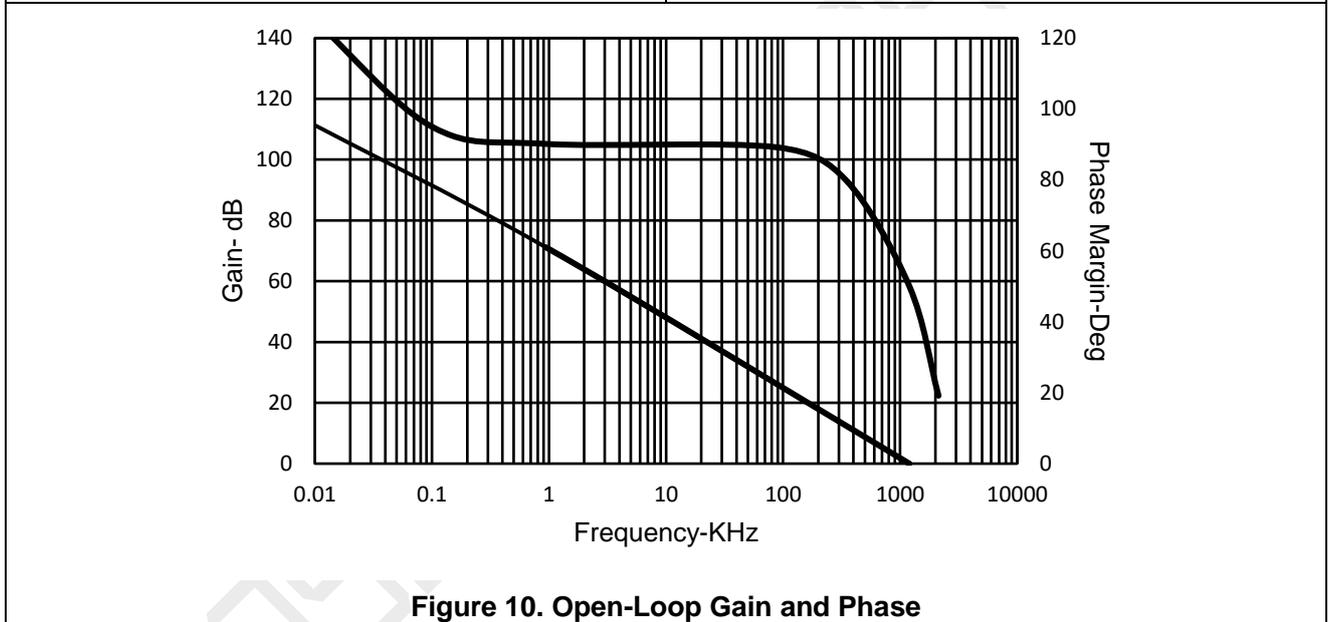
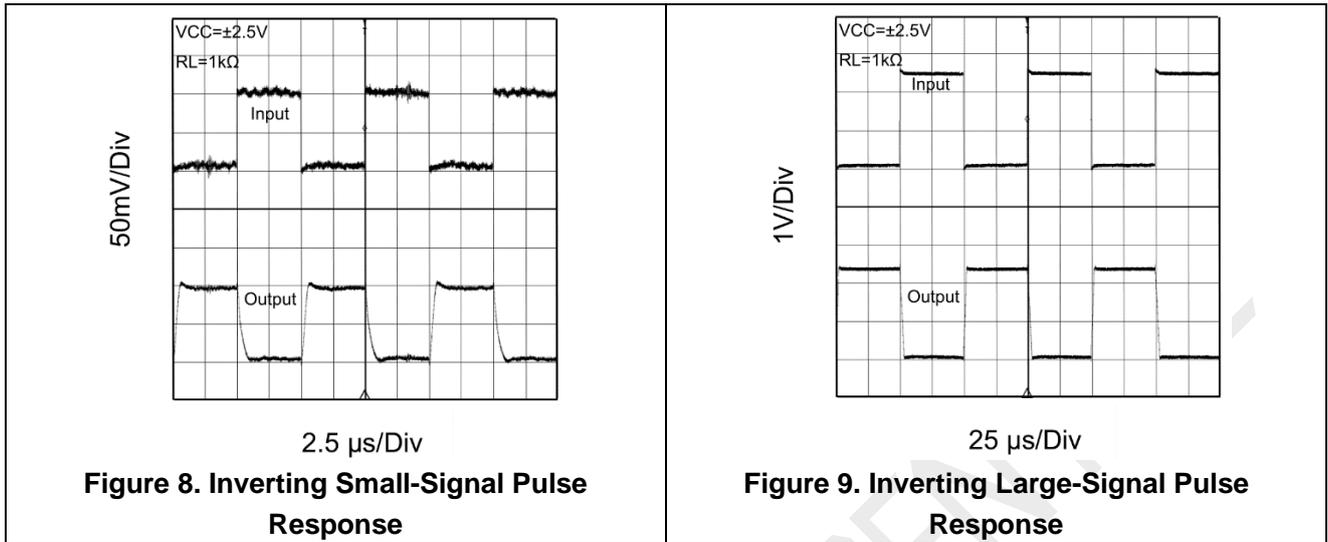
Note 3: 100% production test at $+25^\circ\text{C}$. Specifications over the temperature range are guaranteed by design and characterization.

TYPICAL PERFORMANCE CHARACTERISTICS

(At $T_A = 25^\circ\text{C}$, $+V_S = +2.5\text{V}$, $-V_S = -2.5\text{V}$, $R_L = 2\text{K}\Omega$, $C_L = 100\text{pF}$, unless otherwise noted.)



TYPICAL PERFORMANCE CHARACTERISTICS



APPLICATIONS INFORMATION

MT060X are low supply voltage CMOS operational Amplifiers. This amplifier has the characteristics of Input/Output full swing, high slew rate, low supply current and high speed operation. Input bias current is very low at 1pA (Typ). MT060X has wide temperature range from -40°C to +85°C. Single or dual supplies as low as 2.1V(±1.05V) and up to 6.0V(±3.0V) can be used.

Voltage follower

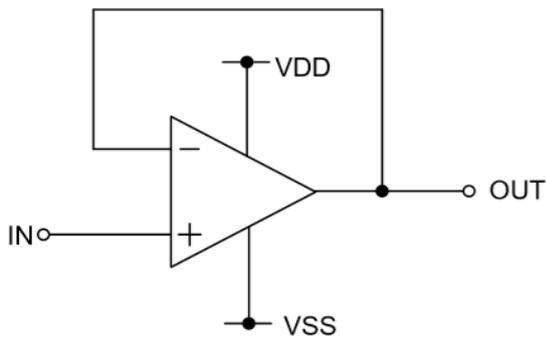


Figure 12. Voltage follower

Voltage gain is 0dB. Using this circuit, the output voltage (OUT) is configured to be equal to the input voltage (IN). This circuit also stabilizes the output voltage (OUT) due to high input impedance and low output impedance. Computation for output voltage (OUT) is shown below. $OUT = IN$.

Inverting amplifier

For inverting amplifier, input voltage (IN) is amplified by a voltage gain and depends on the ratio of R1 and R2. The out-of-phase output voltage is shown in the next expression

$$OUT = -(R2/R1) \cdot IN$$

This circuit has input impedance equal to R1.

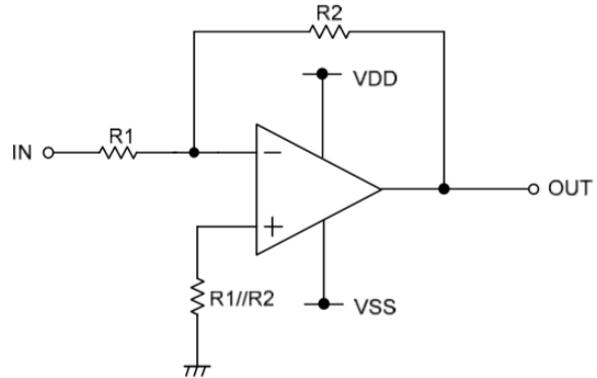


Figure 13. Inverting amplifier circuit

Non-inverting amplifier

For non-inverting amplifier, input voltage (IN) is amplified by a voltage gain, which depends on the ratio of R1 and R2. The output voltage (OUT) is in-phase with the input voltage (IN) and is shown in the next expression.

$$OUT = (1 + R2/R1) \cdot IN$$

Effectively, this circuit has high input impedance since its input side is the same as that of the operational amplifier.

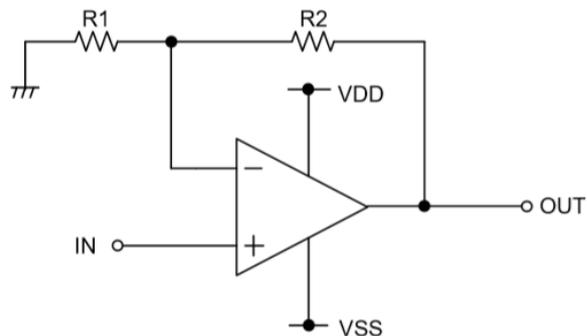
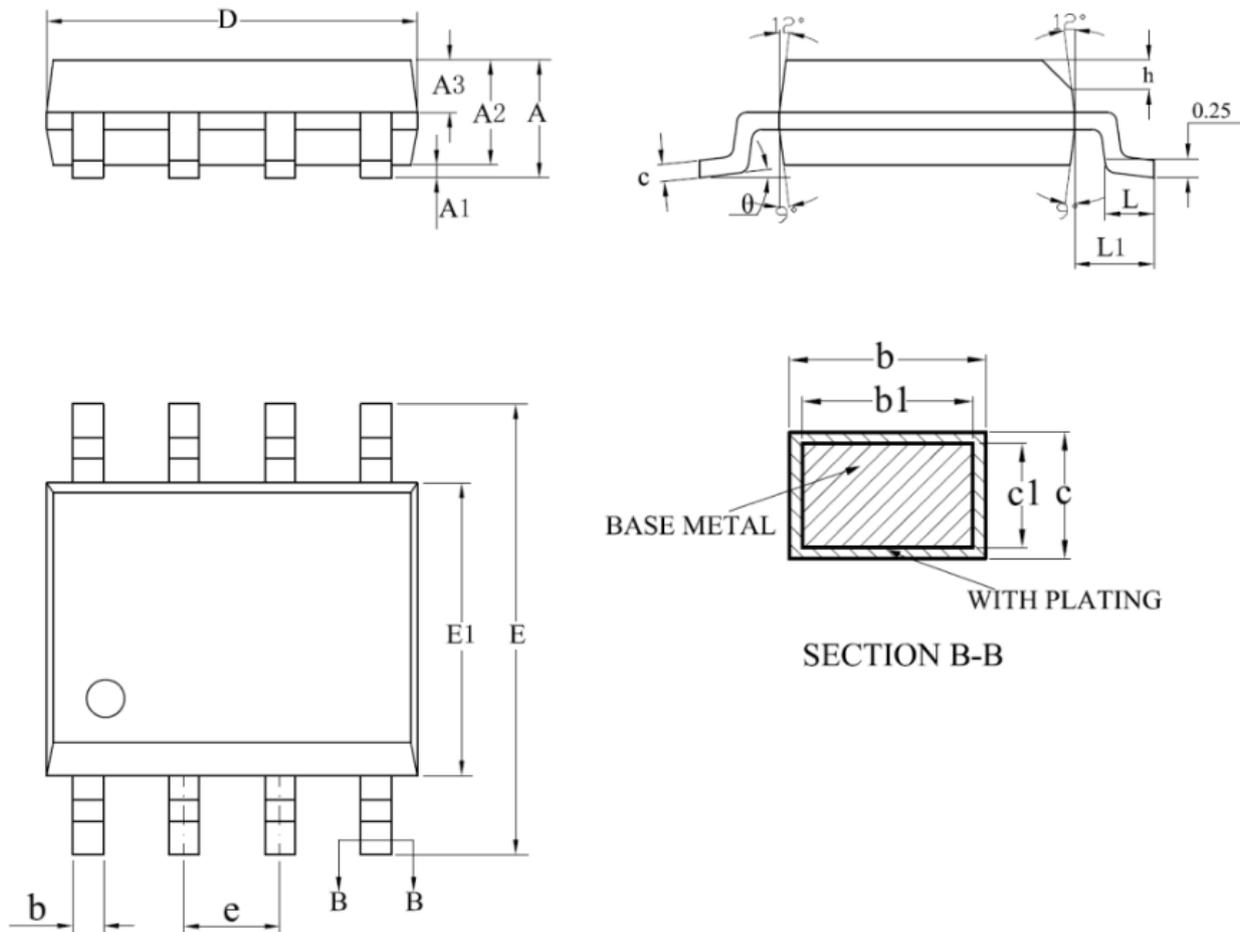


Figure 14. Non-inverting amplifier circuit

PACKAGE DESCRIPTION

SOIC-8



SYMBOL	millimeter		
	min	nom	max
A	-	-	1.75
A1	0.10	-	0.23
A2	1.30	1.40	1.50
A3	0.60	0.65	0.70
b	0.39	-	0.47
b1	0.38	0.41	0.44
c	0.20	-	0.24
c1	0.19	0.20	0.21
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.27BSC		
h	0.25	-	0.50
L	0.50	-	0.80

L1	1.05REF		
θ	0	-	8°

NOTE:

- 1.All linear dimensions are in inches (millimeters).
- 2.This drawing is subject to change without notice.
- 3.Body length does not include mold flash,protrusions,or gate burrs.mold flash,protrusions,or gate burrs shall not exceed 0.006 (0.15) each side.
- 4.Body width does not include interlead flash.interlead flash shall not exceed 0.017 (0.43)each side.

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